

03500.101486.

PATENT APPLICATION



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
TOMONARI NAKAYAMA, et al. )  
Application No.: 10/559,799 )  
371(c) Date: December 8, 2005 )  
For: FIELD EFFECT TRANSISTOR )  
AND PRODUCTION PROCESS :  
THEREOF ) May 22, 2007

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

This is further to the Information Disclosure Statement dated November 30, 2006. An updated Form PTO-1449 is enclosed herewith to correct an error in the citation for the Shimoda document.

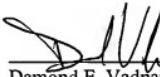
A copy of the Shimoda document, together with a partial English translation, was previously submitted.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /B.J./

Applicants request that the above information be considered by the Examiner and that a copy of the enclosed Form PTO-1449 be initialed and returned indicating that such information has been considered.

Applicants' undersigned attorney may be reached in our Costa Mesa, California office at (714) 540-8700. All correspondence should continue to be directed to our below-listed address.

Respectfully submitted,



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Damond E. Vadnais  
Attorney for Applicants  
Registration No.: 52,310

FITZPATRICK, CELLA, HARPER & SCINTO  
30 Rockefeller Plaza  
New York, New York 10112-3800  
Facsimile: (212) 218-2200

**\*EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Sheet 1 of 1

**Form #61**

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /B.J./